Tele: 7249520184/2103

Army Institute of Technology

Pune-411015

AIT/0810/214/2025-26/Proj

Date:

Sept. 2025

Due Date:

30 Sept. 2025

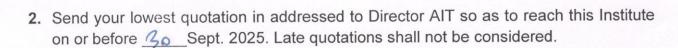
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4	A Foundation Course in Human Values and Professional Ethics, Ed.3 Revised Edition, 2023	R R Gaur, R Asthana and G P Bagaria	Excel Books	05		
5	Teachers Manual for A Foundation Course in Human Values and Professional Ethics, Ed.3 Revised Edition 2024	R R Gaur, R Asthana and G P Bagaria	UHV Publications	05		
6	Community-Based Research: Teaching for Community Impact, 2016	Beckman, M and Long, J F	Stylus Publishing	02		
7	Design Thinking: Understanding How Designers Think and Work, 2011	Nigel Cross	Bloomsbury Publishing	10		

21	Sprint: How to Solve Big Problems and Test	Jake Knapp	Random House	10	
20	The Design of Everyday Things, 2013	Don Norman	Basic Books Publications	05	
19	Principles of Engineering Economics with Applications, Ed.2	Zahid A Khan, Arshad N Siddiquee, Brajesh Kumar, Mustufa H Abidi	Cambridge University	05	
18	Digital Design	M M Mano	Prentice Hall India	05	
17	Digital Design- Principles and Practices	John F Wakerly	Pearson Publication	06	
16	Linear Integrated Circuits	Salivahanan, Kanchana Bhaskaran	Tata McGraw Hill	10	
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12	Modern Electronics Instrumentation and Measurement Techniques, Ed.3	Cooper and Helfrick	Pearson Education India	05	
11	CMOS VLSI Design: A Circuits and Systems Perspective, Ed.4	H E Weste, David Money Harris	Pearson Publication	10	
10	Digital Systems Design Using VHDL, Ed.2	Charles H Roth	Thompson Learning	05	
9	MicroC/OS II, The Real-Time Kernel, Ed.2	Jean J Labrosse	McGraw Hill	02	
8	Change by Design: How Design Thinking Transforms Organizations and Inspires Innovation, 2009	Tim Brown, Harper	Collins Publications	10	

	New Ideas in Just Five Days				
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26	Mathematical Methods for Physics and Engineering, 2006	K F Riley, M P Hobson, and S J Bence	Cambridge University Press	05	
27	CMOS Circuit Design, Layout, and Simulation, Ed.3	R Jacob Baker	Wiley-IEEE Press	01	
28	Digital System Design with FPGA: Implementation Using Verilog and VHDL	Cem Unsalan and Bora Tar	McGraw-Hill	01	
29	Electronics Instrumentation, Ed.2	H S Kalsi	ТМН	03	
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(a) The price quoted should be inclusive of all taxes and delivery at Army Institute of Technology, Dighi, Pune-411015.

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- (c) Supply should be done within four weeks of the date of supply order.
- (d) Quotations will be opened by Board of Officers detailed by AIT. Please attach GST certificate, Pan card, Shop Act etc. if any.
- (e) Payments for supplies will be made direct to the supplier by NEFT only after receipt of the stores in good condition and on verification by a board of officers.
- (f) Printed conditions of the tenders/quotations shall not be binding on us.
- (g) The acceptance of quotations will rest with the Director, Army Institute of Technology, Dighi, Pune-411015, who does not bind himself to accept the lowest quotation and reserves the right himself to reject or partially accept any or all quotations received without assigning any reason.
- (h) Quotations are liable to be rejected if any of the above conditions are not compiled with.

(MK Prasad)

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Jt. Director for Director